CgC



PATENT APPLICATION Attorney's Doc. No. 9903-040

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent No.:

6,736,306 B2

Inventor:

Byun et al.

Issued:

Aug. 4, 1998

For:

SEMICONDUCTOR CHIP PACKAGE COMPRISING ENHANCED PADS

Ser. No.:

10/053,278

Filed: Jan. 16, 2002

Group Art Unit:

1725

Examiner: Kiley Stoner

TRANSMITTAL LETTER

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Certificate

OCT 2 6 2004

Enclosed for filing are the following:

of Correction

Request for Certificate of Correction

Certificate of Correction Form PTO 1050

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number 13-1703.

Respectfully submitted,

MARGER JOHNSON & McCOLLOM, P.C.

Hosoon Lee

Limited Recognition Under 37 CFR § 10.9(b)

MARGER JOHNSON & McCOLLOM, P.C. 1030 SW Morrison Street Portland, OR 97205 503-222-3613 Customer No. 20575

Date: October 18, 2004

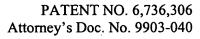
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Alyssa Johnson

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REQUEST FOR CERTIFICATE OF CORRECTION

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Upon comparison of the above-identified original Letters Patent with applicant's copy of the application therefor, the following error in the original Letters Patent has been noted:

Column 5, line 63, "on the substrates, wherein" should read --on the substrate, wherein--;

Applicant respectfully requests that a Certificate of Correction calling attention to the above error be issued pursuant to Rule 322 of the Patent Office and Title 35, United States Code, Section 254. Two copies of a Certificate of Correction form are enclosed.

Respectfully submitted,

MARGER JOHNSON & McCOLLOM, P.C.

Hosoon Lee

Limited Recognition Under 37 CFR § 10.9(b)

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d Johnson

Date: Ogtober 18, 2004

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(Also Form PTO-1050)

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO

6,736,306 B2_

DATED

May 18, 2004

INVENTOR(S):

Byun et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 5, line 63, "on the substrates, wherein" should read -- on the substrate, wherein--;

MAILING ADDRESS OF SENDER: Marger Johnson & McCollom 1030 S.W. Morrison Street Portland, Oregon 97205 PATENT NO. 6,736,306

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO

6,736,306 BZ

DATED

May 18, 2004

INVENTOR(S):

Byun et al.

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Column 5, line 63, "on the substrates, wherein" should read -- on the substrate, wherein--;

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